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NOTICE OF ALLOWANCE AND FEE(S) DUE

877 7590 12/05/2008

IBM CORPORATION, T.J. WATSON RESEARCH CENTER
P.O. BOX 218
YORKTOWN HEIGHTS, NY 10598

EXAMINER

LEE, ANDREW CHUNG CHEUNG

ART UNIT

PAPER NUMBER

2419

DATE MAILED: 12/05/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/527,493

09/28/2005

Jan Van Lunteren

CH920010053US1

4782

TITLE OF INVENTION: METHOD AND APPARATUS FOR DEEP PACKET PROCESSING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/05/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

**Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE
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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

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877 7590 12/05/2008

**IBM CORPORATION, T.J. WATSON RESEARCH CENTER
P.O. BOX 218
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Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)
(Signature)
(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/527,493 09/28/2005 Jan Van Lunteren CH920010053US1 4782

TITLE OF INVENTION: METHOD AND APPARATUS FOR DEEP PACKET PROCESSING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO \$1510 \$300 \$0 \$1810 03/05/2009

EXAMINER	ART UNIT	CLASS-SUBCLASS
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LEE, ANDREW CHUNG CHEUNG 2419 370-397000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, 1 _____
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed. 2 _____
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE (B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent) : ☐ Individual ☐ Corporation or other private group entity ☐ Government

4a. The following fee(s) are submitted:

- ☐ Issue Fee
☐ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies _____

4b. Payment of Fee(s); (Please first reapply any previously paid issue fee shown above)

- ☐ A check is enclosed.
☐ Payment by credit card. Form PTO-2038 is attached.
☐ The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____

Date _____

Typed or printed name _____

Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/527,493	09/28/2005	Jan Van Lunteren	CH920010053US1	4782
877	7590	12/05/2008	EXAMINER	
IBM CORPORATION, T.J. WATSON RESEARCH CENTER P.O. BOX 218 YORKTOWN HEIGHTS, NY 10598			LEE, ANDREW CHUNG CHEUNG	
			ART UNIT	PAPER NUMBER
			2419	
DATE MAILED: 12/05/2008				

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b) (application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 616 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 616 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability	Application No.	Applicant(s)	
	10/527,493	VAN LUNTEREN, JAN	
	Examiner	Art Unit	
	Andrew C. Lee	2419	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Amendment dated 11/05/2008.
2. ☒ The allowed claim(s) is/are 1.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
 - * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| 1. <input type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input type="checkbox"/> Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____ | 7. <input checked="" type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

/Andrew C Lee/
Examiner, Art Unit 2419

DETAILED ACTION

Response to Amendment

1. The objections to the drawings, specification, claims and rejection under 112 second paragraph for claim 1 were withdrawn, since the objections and rejection were overcome by Application's Amendment filed on 11/05/2008.

EXAMINER'S AMENDMENT

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Vazken Alexanian on 11/20/2008.

3. The application has been amended as follows:

- Claim1 has been amended as following:

1. (Currently Amended) A method implemented in a chip embedded apparatus for performing deep processing on an input variable word bit chain, said method comprising the steps of:

creating a state table based on at least one initial state, and a final state, said state table having entries, each said entry defining a state-transition rule comprising a s-bit current state, a n-bit word of the input variable word bit chain and a s-bit next state;

- converting the entries of the state table into a reduced number of state-transition rule entries, each entry containing a ternary match condition expressed as a s+n-bit test value and a s+n-bit test mask to be applied to a the current state and an

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input word in combination, said each entry further containing the s-bit next state, to provide a reduced state table with entries;

- ordering the reduced state table entries obtained by the execution of the converting step, in a prioritized order, with most frequently used state- transition rules having the highest priority;

- initializing a current state as being the initial state and a the first word of the chain being a current input word;

- testing a the current state and a the current input in combination, against the test value, using the test mask, in all the entries of the reduced state table until a match is found on at least one entry;

- if multiple entries match, selecting one entry with the highest priority to be a selected matching entry;

- if the next state read in the state-transition rule of the selected matching entry is not a final state, defining a next word of the input word chain as being a current input and the next state being the current state;

- repeating the testing, selecting and defining steps until a final state is found, and before the initializing step comprising the further steps of:

- defining as a hash index for the reduced state table, a set of i bit locations inside the s-bit current state and an the input n-bit word in combination, and an integer N, such that, at most, N table entries can match a hash index value;

- creating a compressed state table, indexed by the hash index, having 2^i entries, each entry corresponding to one value of the hash index, and each having a maximum of N state-transition rules of the reduced state table corresponding to the same hash index value and written in a priority order;

- saving an s + n bit index mask corresponding to the hash index, and saving a base address pointer of the compressed state table; and

- said testing step further comprises an initial step of identifying the hash index of a current state and current input in combination, using the index mask, and testing the hash index to identify a corresponding entry in the compressed state table located using the base address pointer, the following testing step against the test value and the

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following steps being performed on the maximum of N state-transition rules of the identified said corresponding entry in the compressed state table,

- dividing the compressed state table into more than one compressed state sub-table;
- extending in each of the compressed state sub-tables, each state-transition rule with a corresponding index mask and a base address pointer of the compressed state sub-table of the next state in said state-transition rule;
- initializing a current compressed state sub-table base address pointer, and
- the base address pointer of a matching entry becoming the current base address pointer of the compressed state of the next state

Allowable Subject Matter

4. Claim 1 is allowed.

5. The following is an examiner's statement of reasons for allowance:

The prior art made of record, in single or in combination, fails to teach explicitly the limitations of:

- “- creating a state table based on at least one initial state, and a final state, said state table having entries, each said entry defining a state-transition rule comprising a s-bit current state, a n-bit word of the input variable word bit chain and a s-bit next state;
- converting the entries of the state table into a reduced number of state-transition rule entries, each entry containing a ternary match condition expressed as a s+n-bit test value and a s+n-bit test mask to be applied to a the current state and an input word in combination, said each entry further containing the s-bit next state, to provide a reduced state table with entries;

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- ordering the reduced state table entries obtained by the execution of the converting step, in a prioritized order, with most frequently used state- transition rules having the highest priority;
- initializing a current state as being the initial state and a the first word of the chain being a current input word;
- testing a the current state and a the current input in combination, against the test value, using the test mask, in all the entries of the reduced state table until a match is found on at least one entry;
- if multiple entries match, selecting one entry with the highest priority to be a selected matching entry;
- if the next state read in the state-transition rule of the selected matching entry is not a final state, defining a next word of the input word chain as being a current input and the next state being the current state;
- repeating the testing, selecting and defining steps until a final state is found, and before the initializing step comprising the further steps of:
 - defining as a hash index for the reduced state table, a set of i bit locations inside the s -bit current state and an the input n -bit word in combination, and an integer N , such that, at most, N table entries can match a hash index value;
 - creating a compressed state table, indexed by the hash index, having 2^i entries, each entry corresponding to one value of the hash index, and each having a maximum of N state-transition rules of the reduced state table corresponding to the same hash index value and written in a priority order;
 - saving an $s + n$ bit index mask corresponding to the hash index, and saving a base address pointer of the compressed state table; and
- said testing step further comprises an initial step of identifying the hash index of a current state and current input in combination, using the index mask, and testing the hash index to identify a corresponding entry in the compressed state table located using the base address pointer, the following testing step against the test value and the following steps being performed on the maximum of N state-transition rules of the identified said corresponding entry in the compressed state table,

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- dividing the compressed state table into more than one compressed state sub-table;
- extending in each of the compressed state sub-tables, each state-transition rule with a corresponding index mask and a base address pointer of the compressed state sub-table of the next state in said state-transition rule;
- initializing a current compressed state sub-table base address pointer, and
- the base address pointer of a matching entry becoming the current base address pointer of the compressed state of the next state” as disclosed in claim 1.

6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew C. Lee whose telephone number is (571)272-3131. The examiner can normally be reached on Monday through Friday from 8:30am - 5:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner’s supervisor, Edan Orgad can be reached on (571) 272-7884. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Andrew C Lee/
Examiner, Art Unit 2419
<11/12/2008-11/20/2008>

/Edan Orgad/
Supervisory Patent Examiner, Art Unit 2419